

8. (Amended) The high-frequency amplifying device according to claim 6, wherein said current subtracting circuit has a current mirror circuit for allowing a current having a value corresponding to the amplitude measured by said measuring circuit to conduct into one end thereof and according to the current, inputting another current set based on a ratio between junction areas and a source voltage from the other end thereof.

#### REMARKS

Favorable consideration of this application, in view of the following comments and as presently amended, is respectfully requested.

The present Preliminary Amendment is submitted to correct for minor informalities in Claims 4 and 8. The changes made to Claims 4 and 8 are deemed to be self-evident from the original disclosure, and thus are not deemed to raise any issues of new matter.

The present application is believed to be in condition for a full and thorough examination on the merits. An early and favorable consideration of the present application is hereby respectfully requested.

Respectfully submitted,

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IN THE CLAIMS

Please amend Claims 4 and 8 to read as follows:

--4. (Amended) The high-frequency amplifying device according to claim 2, wherein said current adding circuit has a current mirror circuit for allowing a current having a value corresponding to the amplitude measured by said measuring circuit to conduct into one end thereof and according to the current, outputting another current set based on a ratio between junction areas [of the current mirror circuit] and a source voltage from the other end thereof.

8. (Amended) The high-frequency amplifying device according to claim 6, wherein said current subtracting circuit has a current mirror circuit for allowing a current having a value corresponding to the amplitude measured by said measuring circuit to conduct into one end thereof and according to the current, inputting another current set based on a ratio between junction areas [of the current mirror circuit] and a source voltage from the other end thereof.--